

Dual- and Quad-Channel  
FMC High-Speed  
SFP/SFP+ Adapter



**F M C - S F P +**

- These FMC cards are compatible with standard VITA 57.1 and can be mounted on carrier boards - DAMC-FMC2ZUP or DAMC-FMC25 - for high speed communication
- The FMC-2SFP+ and FMC-4SFP+ boards have a configurable oscillator and a wide I/O operating range (1.5 V to 3.3 V on  $V_{ADJ}$ )
- Available in 2-channel or 4-channel SFP/SFP+ versions

**FEATURES**

- **FPGA Mezzanine Card**
- **HPC FMC Module (Vita 57.1 compliant) with HPC compatibility**
- **Wide I/O operating voltage range:  $V_{ADJ}$  can vary from 1.5 V to 3.3 V**
- **True level conversion of all SFP+ module pins including I2C bus lines**
- **I2C-Controlled oscillator (10-280 MHz)**
- **Available as 2-channel or 4-channel version**
- **Fits on any FMC carrier without front-panel modification**
- **Individual module status via LEDs**
- **Autonomous Mode: Clock speed setting and transmitter activation via DIP switches**

**APPLICATIONS**

- **Fast Communication Interface**
- **Accelerator Diagnostics and Control**

**FMC-SFP+.** The FMC-SFP4 is a cost-efficient FPGA Mezzanine Card (FMC) designed according to ANSI/VITA 57.1 standard.

It offers two or four SFP/SFP+ module slots. All module pins are translated to the FMC Carrier Voltage ( $V_{ADJ}$ ) that can be in the range of 1.5 V to 3.3 V.

With this low operating voltage it can be used on almost all carriers. The module is designed as an HPC.

The components are placed to be compatible with carriers that have components under the FMC module. The SFP+ cage is placed to fit to all carriers without front panel modification (card is only 16 mm longer than described by FMC standard).

The module features an I2C-controlled LVDS (Low-Voltage Differential Signaling) oscillator chip that operates in the range from 10 to 280 MHz.

Automatic configuration of clock oscillator and module pins for standalone operation is selectable via on-board jumpers.





Commercially available versions are the dual-channel FMC-2SFP+ with standard VITA 57.1 bezel and the quad-channel FMC-4SFP+.

Please check the FMC and MTCA.4 sections on our website [www.caenels.com](http://www.caenels.com) to check for news, updates and additional information on this specific cards and other FMC-based solutions.



**About Us**

ELS Instruments (formerly CAEN ELS) is a leading company in the design of power supplies and state-of-the-art complete electronic systems for the Physics research world, having its main focus on dedicated solutions for the particle accelerator community and high-end industrial applications.

-  Power Supply Systems
-  Precision Current Measurements
-  Beamline Electronics Instrumentation
-  FMC and MicroTCA

**ELS Instruments srl**

Via Karl Ludwig von Bruck 32  
34144 - Trieste (TS)  
Italy

[info@caenels.com](mailto:info@caenels.com)

[www.els-instruments.com](http://www.els-instruments.com)

[www.caenels.com](http://www.caenels.com)



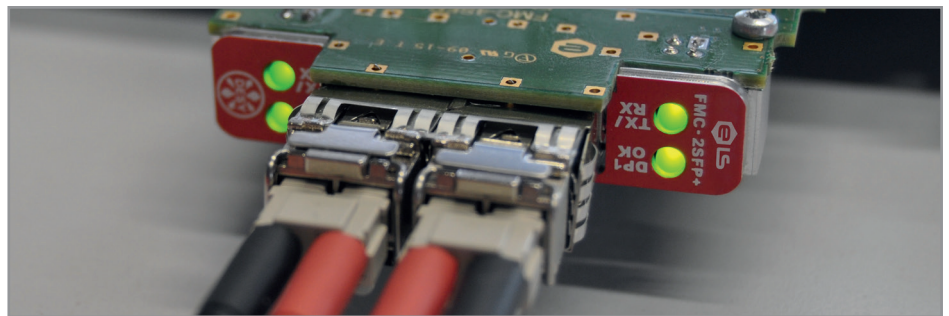
Designed and Licensed  
by **DESY**

**Deutsches Elektronen-Synchrotron**  
Ein Forschungszentrum der Helmholtz-Gemeinschaft

*Technical Specifications*

**FMC-SFP+**

	Dual-channel	Quad-channel
<b>Board Type</b>	FPGA Mezzanine Card - VITA 57.1	
<b>FMC Connector Type</b>	HPC - High Pin Count	
<b>Number of SFP/SFP+ channels</b>	2	4
<b>V<sub>ADJ</sub> Range</b>	1.5 V ... 3.3 V	
<b>On-board Oscillator Frequency Range</b>	10 MHz ... 280 MHz	
<b>On-board Oscillator Configuration</b>	I2C protocol	
<b>Additional Features</b>	<ul style="list-style-type: none"> <li>- Link Status indication via front panel or bottom-emitting LEDs</li> <li>- <u>Autonomous Mode</u>: clock speed setting and transmitter activation via DIP switches</li> </ul>	
<b>Mechanical Dimensions</b>	FMC - VITA 57.1	



**Dual-channel FMC-SFP+ version during operation**



**Quad-channel FMC-SFP+ version**

**Ordering Codes**

Ordering Code	Acronym	Description
FMC4SFP2XAAA	FMC-2SFP+	Dual-channel SFP/SFP+ Adapter FMC
FMC4SFP4XAAA	FMC-4SFP+	Quad-channel SFP/SFP+ Adapter FMC