



8-channel 20-bit
Bipolar Picoammeter
with MTCA.4 Rear I/O Series



AMC-PICO-8

- The AMC-PICO-8 series is composed of a full-size standard AMC board with 8 Bipolar 20-bit current-input electrometer channels
- Input channels are capable of floating up to ± 300 V respect to chassis ground
- Two independent full-scale ranges for each channel

FEATURES

- Double-width AMC board
- MTCA.4 standard
- High resolution Multi-Channel current measurements
- 8 Bipolar Current-Input Channels
- Two independent full-scale ranges
- Up to 1 MSPS simultaneous and independent sampling
- 20-bit resolution
- Low conversion time delay
- Input channels floating up to ± 300 V
- Data processing on Virtex-5 FPGA
- Board Management on Spartan-6 FPGA
- uRTM D1.1 connectivity
- External Clock input on front panel

APPLICATIONS

- Photon Beam Position Monitors
- Multi-Channel Fast Current Acquisition
- Detector Readout
- Accelerator Controls

AMC-PICO-8. The AMC-PICO-8 is an eight-channel double-width picoammeter AMC board in MTCA.4 format. The analog front-end allows measuring bipolar currents up to 1 mA in the standard version with a maximum sampling rate of **1 MSPS** per channel.

The analog front-end is composed of a specially designed transimpedance input stage for current sensing combined with analog signal conditioning and filtering stages making use of state-of-the-art electronics. The **20-bit resolution** is obtained from independent, simultaneous sampling and low-delay SAR (Successive Approximation Register) Analog to Digital Converters (ADCs).

Each channel has **two full-scale measuring ranges** (up to ± 1 mA and ± 1 μ A respectively in the

standard version) and the current source can be **floating up to ± 300 V** respect to the chassis ground. This floating capability of the inputs is perfectly suitable for applications where the detector or current source needs to be biased.

The analog front end is designed in order to achieve low noise, low temperature dependence and very small unbalance between channels. The analog characteristics are further improved with calibration. Calibration data are stored in the **on-board EEPROM** memory and are loaded in the signal processing logic on power-up.

The on-board Virtex-5 FPGA performs the conversion from "raw" values acquired from ADCs to a single-precision floating point numbers, representing the measured current in Ampere. The floating point format is highly suitable for additional post-

